



**QIS COLLEGE OF ENGINEERING AND TECHNOLOGY  
(AUTONOMOUS)**

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Ponduru Road, Vengamukkapalem, Ongole, Prakasam (Dt), Andhra Pradesh - 523272

**22.06.2021**

**Report on TDMA/CDMA Systems**

<b>Title of the Programme</b>	:	Seminar on TDMA/CDMA Systems
<b>Resource Person</b>	:	<b>Mr.M.Chiranjeevi Muppala,</b> Research Associate, SENSE, VIT University, Chennai.
<b>Date and Time</b>	:	17.06.2021 & 2.30 pm to 3.30 pm
<b>Mode of Session</b>	:	Online
<b>Meet App</b>	:	Google Meet
<b>Total number of Participants</b>	:	Registered : 250 Attended : 220 (Students) + 2 (Faculty members)

**Brief details of the event:**

QISCET – EDC & IIC conducted an online session on TDMA/CDMA Systems on 17<sup>th</sup> June 2021. The speaker of the session was Mr.M.Chiranjeevi Muppala. He highlighted the importance of the topic for the students looking to begin their own design development and how they can utilize this session to understand the concept of wireless communication – TDMA/CDMA for the design of antennas that they can design for their ideas.

**Response of the audience:** The students gained an in-depth understanding of the topic and how to design the TDMA/CDMA concepts and make into a prototype. The learning was quite evident from the level of questions asked by the students to the expert. He was able to arouse interest of the students in the area and the

Session became a wonderful dialogue-based session where the students were asking regular questions and expert was generous enough to answer almost all their queries.

**Faculty Coordinator Name & Email ID:** Dr.V.S.Nishok, nishok.vs@qiscet.edu.in

**Number of participants:** 2 faculty members and 220 students

**Learning Outcomes:**

Students gained in-depth knowledge in relation to the wireless communication CDMA/TDMA technology and to design and their importance as a part of the curriculum. The topic dealt with core areas of the communication.

**Expert Narration:**

Mr.M.Chiranjeevi Muppala, started his session with a hands-on example of the recent prototype that they had very recently developed in their research purpose. The expert shared the entire journey of how to simulate the logic from the ideation stage to a prototype. The expert shared all the stages involved in design of communication protocol to conversion into a circuit through this practical example which ensured that the students could relate to the concept and understand the process of converting an idea into a circuit model.

Date: 22.06.2021

Place: Ongole

**Dr.V.S.Nishok**