



**QIS COLLEGE OF ENGINEERING AND TECHNOLOGY
(AUTONOMOUS)**

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Ponduru Road, Vengamukkapalem, Ongole, Prakasam (Dt), Andhra Pradesh - 523272

10.11.2021

Report on Edsim 51 Simulator

Title of the Programme	:	Seminar on Edsim 51 Simulator	
Resource Person	:	Mr. K.Venkata Reddy, Ph.D. Scholar, Hyderabad.	
Date and Time	:	06.11.2021 & 12.30 pm to 1.30 pm	
Mode of Session	:	Online	
Meet App	:	Google Meet	
Total number of Participants	:	Registered	: 180
		Attended	: 155 (Students) + 2 (Faculty members)

Brief details of the event:

QISCET – EDC & IIC conducted an online session on Edsim 51 Simulator on 6th November 2021. The speaker of the session was Mr. K.Venkata Reddy. He highlighted the importance of the topic for the students looking to begin their own design development and how they can utilize this session to understand the concept of Edsim 51 simulator for the logics that they can design for their ideas.

Response of the audience: The students gained an in-depth understanding of the topic and how to design the circuit using Edsim 51 Simulator in selecting and into a prototype. The learning was quite evident from the level of questions asked by the students to the expert. He was able to arouse interest of the students in the area and the

Session became a wonderful dialogue-based session where the students were asking regular questions and expert was generous enough to answer almost all their queries.

Faculty Coordinator Name & Email ID: Dr.V.S.Nishok, nishok.vs@qiscet.edu.in

Number of participants: 2 faculty members and 155 students

Learning Outcomes:

Students gained in-depth knowledge in relation to the edsim 51 simulator Design and their importance as a part of the curriculum. The topic dealt with core areas of the microcontroller.

Expert Narration:

Mr.K.Venkata Reddy, started his session with a hands-on example of the recent prototype that they had very recently developed. The expert shared the entire journey of how to simulate the logic from the ideation stage to a prototype. The expert shared all the stages involved in design of Edsim 51 simulator to conversion into a circuit through this practical example which ensured that the students could relate to the concept and understand the process of converting an idea into a circuit model.

Date: 10.11.2021

Place: Ongole

Dr.V.S.Nishok