

**QIS College of Engineering & Technology**  
**LOW POWER VLSI DESIGN CHALLENGES**

**OVERVIEW**

<b>Objective:</b>	To inculcate the knowledge on design challenges in the field of VLSI	<b>Benefit in terms of learning/Skill/Knowledge obtained:</b>	The students and the faculty members gained knowledge on different design challenges involved the VLSI technology
<b>Academic Year:</b>	2020-21	<b>Program driven by:</b>	Self-driven Activity
<b>Month:</b>	June	<b>Program /Activity Name:</b>	Low Power VLSI Design Challenges
<b>Program Type:</b>	Workshop	<b>Other:</b>	null (Ministry of HRD Initiative)
<b>Program Theme:</b>	Design Thinking & Critical Thinking	<b>Other:</b>	NA

<b>Date &amp; Duration (Days):</b>	06/07/2021-06/07/2021-0	<b>External Participants, If any:</b>	0
<b>Student Participants:</b>	73	<b>Faculty Participants:</b>	2
<b>Expenditure Amount, If any:</b>	0	<b>Remark:</b>	NA

### STAR PERFORMER

<b>Faculty:</b>	NA	<b>Student:</b>	N V Vinaykumar
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### ATTACHMENTS

<b>Video:</b>	null	<b>Photograph1:</b>	
<b>Photograph2:</b>		<b>Session plan, If any:</b>	<a href="https://api.mic.gov.in/uploads/institutes/monthlyReport/report/4171-IC201810612.pdf">https://api.mic.gov.in/uploads/institutes/monthlyReport/report/4171-IC201810612.pdf</a>

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