<table>
<thead>
<tr>
<th>SN.NO</th>
<th>Name of Faculty (Principal Investigator)</th>
<th>Name of the Funding agency</th>
<th>Title of the Project</th>
<th>Sanctioned order no.</th>
<th>Sanctioned date</th>
<th>Amount Received (In Rupees)</th>
<th>Amount received (in words)</th>
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<tbody>
<tr>
<td>1</td>
<td>Dr. Balapanur Mouli Chandra</td>
<td>SERB, DST</td>
<td>EXPERIMENTAL INVESTIGATION OF TEMPERATURE DEPENDENT PARAMETERS IN INDIRECT VECTOR CONTROL OF INDUCTION MOTOR AND ESTIMATION USING FPGA</td>
<td>ECR/2016/000843</td>
<td>19/08/2016</td>
<td>28,70,000</td>
<td>Twenty eight lakh seventy thousand</td>
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<td>2</td>
<td>Dr.R.Satya Mehar</td>
<td>SNES Inhouse</td>
<td>Experimental studies on mixed refrigerant cascade freezer</td>
<td>InhouseR&amp;D/QISCET/2015-16/1</td>
<td>9/5/2015</td>
<td>1,02,500</td>
<td>One lakh two thousand five hundred</td>
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<td>3</td>
<td>Dr.B.V.Prasanth</td>
<td>SNES Inhouse</td>
<td>Design of smart power system</td>
<td>InhouseR&amp;D/QISCET/2015-16/2</td>
<td>21/08/2015</td>
<td>1,07,900/-</td>
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<td>4</td>
<td>Dr.Ch.Balaswamy</td>
<td>SNES Inhouse</td>
<td>Design &amp; Implementation of Network on chip in both front-end and back-end</td>
<td>InhouseR&amp;D/QISCET/2015-16/3</td>
<td>14/11/2015</td>
<td>55,400</td>
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<td>5</td>
<td>K.Naik</td>
<td>SNES Inhouse</td>
<td>Design &amp; Implementation of System on chip using Network on chip</td>
<td>InhouseR&amp;D/QISCET/2015-16/4</td>
<td>2/2/2016</td>
<td>75,600/-</td>
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<td>Mr.G.Dayanandam</td>
<td>SNES Inhouse</td>
<td>Defending distributed denial of service attacks</td>
<td>InhouseR&amp;D/QISCET/2015-16/5</td>
<td>10/6/2016</td>
<td>60,400</td>
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<td>7</td>
<td>Dr.D. Bujji Babu</td>
<td>SNES Inhouse</td>
<td>Agricultural data analysis using data mining techniques</td>
<td>InhouseR&amp;D/QISCET/2015-16/6</td>
<td>11/9/2016</td>
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